

**ABSTRACT OF THE DISCLOSURE**

Please replace the abstract with the following new abstract:

Field programmable mixed-signal integrated circuit. A reconfigurable processor integrated circuit includes a processor core is provided that operates on a set of instructions to carry out predefined processes. A plurality of input/output pins are provided for interfacing with external signals. A reconfigurable interface interfaces between the processor core and the input/output pins. The reconfigurable interface is operable to define how each of the plurality of input/output pins interfaces with the processor core and the functionality associated therewith and has associated therewith a non-volatile memory for storing configuration information that can be altered.

**AMENDMENT AND RESPONSE**

S/N 09/885,459

Atty. Dkt. No. CYGL-25,768